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IEEE STD	IEEE Standard		High Performance Interconnects, 2005. Proceedings. 13th Symposium on 17-19 Aug. 2005 Page(s):45 - 50	
			Digital Object Identifier 10.1109/CONECT.2005.10	
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		<b></b>	<ol> <li>Localized congestion control in advanced switching interconnects         Krishnan, V.; Mayhew, D.;         Micro, IEEE         Volume 25, Issue 1, JanFeb. 2005 Page(s):10 - 11         Digital Object Identifier 10.1109/MM.2005.17</li> </ol>	
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# Can memory-less network adapters benefit next-generation infiniband systems?

Dept. of Comput. Sci. & Eng., Ohio State Univ., Columbus, OH, USA Suc. S. Vishnu. A. Jin. H.-W. Panda, D.K. Huang, W.

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Number of Pages: xii+168

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only by 10% and only for message sizes IB -4 KB. These memory-less adapters allow more efficient use of overall system memory and show latency is the same for both local NIC memory and host memory. Under heavy I/O bus usage, the latency of a scatter operation increased whose adapters can be chosen to run with or without local NIC memory. Our investigations reveal that on these systems, the memory fetch design experiments, analyze the performance of various communication patterns and end applications on PCLExpress based systems, local NIC memory. This memory was used to store internal information. This memory increases the overall cost of the NIC. In this paper we of the host memory by network interface cards (NICs). Earlier generation InfiniBand adapters used to have an external DIMM attached as Recently, PCL Express, which is the third generation high-performance I/O bus used to interconnect peripheral devices, has been released. network adapters can benefit next generation InfiniBand systems practically no performance impact (less than 0.1%) for the NAS parallel benchmarks on 8 processes. These results indicate that memory-less The third generation of InfiniBand adapters allow applications to take advantage of PCI-Express. PCI-Express offers very low latency access InfiniBand is emerging as a high-performance interconnect. It is gaining popularity because of its high performance and open standard.

mdez Terms

Controlled Indexing

computer architecture file organisation network interfaces peripheral interfaces

Non-controlled Indexing

bus high-performance interconnect host memory memory-less adapter network interface card peripheral device InfiniBand NAS parallel benchmark NIC PCI-Express external DIMM attachment generation high-performance I-O

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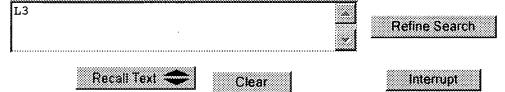
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(PCI adj 1 Express) same mode	5

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<u>L3</u> 11 and L2	11	<u>L3</u>
<u>L2</u> (PCI adj1 Express) same mode	57	<u>L2</u>
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